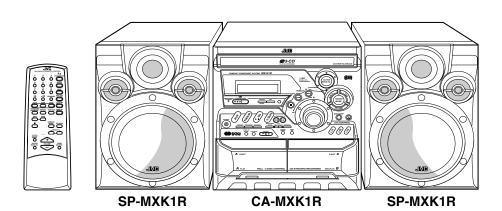
MX-K1R

JVC SERVICE MANUAL

COMPACT COMPONENT SYSTEM

MX-K1R







| Area Suffix | | | | | | |
|--------------------------|--|--|--|--|--|--|
| B U.K. | | | | | | |
| E Continental Europe | | | | | | |
| EN Northern Europe | | | | | | |
| EE ····· Russia | | | | | | |
| EV ······ Eastern Europe | | | | | | |

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-Safety Precautions-

- 1. This design of this product contains special hardware and many circuits and components specially for safety purposes. For continued protection, no changes should be made to the original design unless authorized in writing by the manufacturer. Replacement parts must be identical to those used in the original circuits. Services should be performed by qualified personnel only.
- 2. Alterations of the design or circuitry of the product should not be made. Any design alterations of the product should not be made. Any design alterations or additions will void the manufacturer's warranty and will further relieve the manufacture of responsibility for personal injury or property damage resulting therefrom.
- 3. Many electrical and mechanical parts in the products have special safety-related characteristics. These characteristics are often not evident from visual inspection nor can the protection afforded by them necessarily be obtained by using replacement components rated for higher voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in the Parts List of Service Manual. Electrical components having such features are identified by shading on the schematics and by (A) on the Parts List in the Service Manual. The use of a substitute replacement which does not have the same safety characteristics as the recommended replacement parts shown in the Parts List of Service Manual may create shock, fire, or other hazards.
- 4. The leads in the products are routed and dressed with ties, clamps, tubings, barriers and the like to be separated from live parts, high temperature parts, moving parts and/or sharp edges for the prevention of electric shock and fire hazard. When service is required, the original lead routing and dress should be observed, and it should be confirmed that they have been returned to normal, after re-assembling.
- 5. Leakage currnet check (Electrical shock hazard testing)

After re-assembling the product, always perform an isolation check on the exposed metal parts of the product (antenna terminals, knobs, metal cabinet, screw heads, headphone jack, control shafts, etc.) to be sure the product is safe to operate without danger of electrical shock.

Do not use a line isolation transformer during this check.

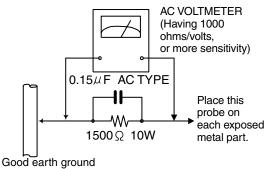
Plug the AC line cord directly into the AC outlet. Using a "Leakage Current Tester", measure the leakage current from each exposed metal parts of the cabinet, particularly any exposed metal part having a return path to the chassis, to a known good earth ground. Any leakage current must not exceed 0.5mA AC (r.m.s.).

Alternate check method

Plug the AC line cord directly into the AC outlet. Use an AC voltmeter having, 1,000 ohms per volt or more sensitivity in the following manner. Connect a 1,500 Ω 10W resistor paralleled by a 0.15 μ F AC-type capacitor

between an exposed metal part and a known good earth ground. Measure the AC voltage across the resistor with the AC voltmeter.

Move the resistor connection to each exposed metal part, particularly any exposed metal part having a return to the chassis, and meausre the AC voltage across the resistor. Now, reverse the plug in the AC outlet and repeat each measurement. Voltage measured any must not exceed 0.75 V AC (r.m.s.). This corresponds to 0.5 mA AC (r.m.s.).



Warning

1. This equipment has been designed and manufactured to meet international safety standards.

2. It is the legal responsibility of the repairer to ensure that these safety standards are maintained.

- 3. Repairs must be made in accordance with the relevant safety standards.
- 4. It is essential that safety critical components are replaced by approved parts.
- 5. If mains voltage selector is provided, check setting for local voltage.

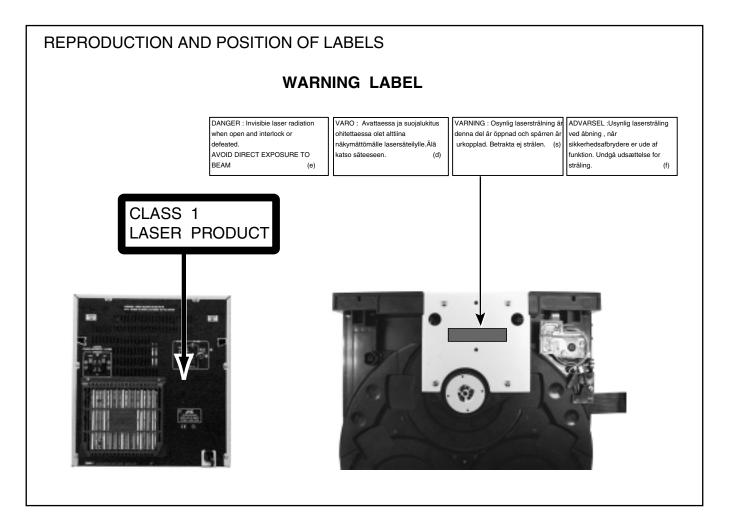
Burrs formed during molding may be left over on some parts of the chassis. Therefore, pay attention to such burrs in the case of preforming repair of this system.

In regard with component parts appearing on the silk-screen printed side (parts side) of the PWB diagrams, the parts that are printed over with black such as the resistor (----), diode (+---) and ICP (----) or identified by the " Λ " mark nearby are critical for safety.

When replacing them, be sure to use the parts of the same type and rating as specified by the manufacturer. (Except the J and C version)

Important for laser products

1.CLASS 1 LASER PRODUCT 5.CAUTION : If safety switches malfunction, the laser is able 2.DANGER : Invisible laser radiation when open and inter to function. lock failed or defeated. Avoid direct exposure to beam. 6.CAUTION : Use of controls, adjustments or performance of procedures other than those specified herein may result in 3.CAUTION : There are no serviceable parts inside the Laser Unit. Do not disassemble the Laser Unit. Replace hazardous radiation exposure. the complete Laser Unit if it malfunctions. 4.CAUTION : The compact disc player uses invisible A CAUTION Please use enough caution not to laserradiation and is equipped with safety switches see the beam directly or touch it whichprevent emission of radiation when the drawer is in case of an adjustment or operation open and the safety interlocks have failed or are de check. feated. It is dangerous to defeat the safety switches. VARNING : Osynlig laserstrålning är denna del är öppnad ADVARSEL : Usynlig laserstråling ved åbning , når och spårren är urkopplad. Betrakta ej strålen. sikkerhedsafbrydere er ude af funktion. Undgå VARO : Avattaessa ja suojalukitus ohitettaessa olet udsættelse for stråling. alttiina näkymättömälle lasersäteilylle.Älä katso ADVARSEL : Usynlig laserstråling ved åpning,når säteeseen. sikkerhetsbryteren er avslott. unngå utsettelse for stråling.



Preventing static electricity

1. Grounding to prevent damage by static electricity

Electrostatic discharge (ESD), which occurs when static electricity stored in the body, fabric, etc. is discharged, can destroy the laser diode in the traverse unit (optical pickup). Take care to prevent this when performing repairs.

2. About the earth processing for the destruction prevention by static electricity

In the equipment which uses optical pick-up (laser diode), optical pick-up is destroyed by the static electricity of the work environment.

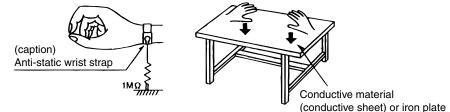
Be careful to use proper grounding in the area where repairs are being performed.

2-1 Ground the workbench

Ground the workbench by laying conductive material (such as a conductive sheet) or an iron plate over it before placing the traverse unit (optical pickup) on it.

2-2 Ground yourself

Use an anti-static wrist strap to release any static electricity built up in your body.



3. Handling the optical pickup

- 1. In order to maintain quality during transport and before installation, both sides of the laser diode on the replacement optical pickup are shorted. After replacement, return the shorted parts to their original condition. (Refer to the text.)
- 2. Do not use a tester to check the condition of the laser diode in the optical pickup. The tester's internal power source can easily destroy the laser diode.

4. Handling the CD changer unit (optical pickup)

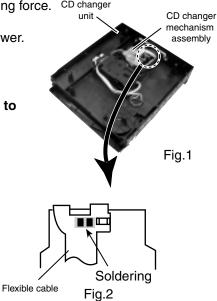
- 1. Do not subject the CD changer unit (optical pickup) to strong shocks, as it is a sensitive, complex unit.
- 2. Cut off the shorted part of the flexible cable using nippers, etc. after replacing the optical pickup. For specific details, refer to the replacement procedure in the text. Remove the anti-static pin when replacing the CD changer unit. Be careful not to take too long a time when attaching it to the connector.
- 3. Handle the flexible cable carefully as it may break when subjected to strong force. CD changer

4. It is not possible to adjust the semi-fixed resistor that adjusts the laser power. Do not turn it

Attention when traverse unit is decomposed

*Please refer to "Disassembly method" in the text for pick-up and how to detach the CD changer mechanism.

- 1. Remove the CD changer unit.
- 2. Disconnect the harness from connector on the CD motor board.
- Solder is put up before the card wire is removed from connector CN601 on the main board as shown in Fig.1 and Fig. 2. (When the wire is removed without putting up solder, the CD pick-up assembly might destroy.)
- 4. Please remove solder after connecting the card wire with CN601 when you install picking up in the substrate.



Disassembly method

Commence disassembly of this set by removing the main units and then proceed to the components and assemblies inside the units.

Replacement of the fuses and the power IC

- Top cover
- CD changer unit
- Front panel assembly
- Chassis unit
- CD changer unit
 - Removing the main PCB
 - Removing the CD changer mechanism assembly
 - Removing the CD pickup
 - Replacing the loading motor and belt of the CD changer tray
 - Replacing the CD tray rotor belt of CD changer, and removing the motor

Front panel assembly

- Removing the cassette deck mechanism
- Removing the earphone jack PCB
- Removing the control/FL PCB
- Removing the switch PCB and ACTIVE BASS EX. switch PCB
- Removing the cassette deck main motor, and replacing the main belts
- Removing the leaf switches of the cassette deck mechanism
- Removing the cassette deck heads
- Chassis unit
 - Removing the 3-pin regulator
 - Removing the power amp and supply PCB and the Power Trans PCB
 - Removing the sub power PCB

<Disassembly of the main blocks of this set>

Replacement of the fuses and the power IC

Replacing the fuses (See Fig.1)

- Prior to performing the following procedure, remove the top cover.
- 1. Replace the fuses inside.
- [Caution] Be sure to use fuses with the specified ratings.

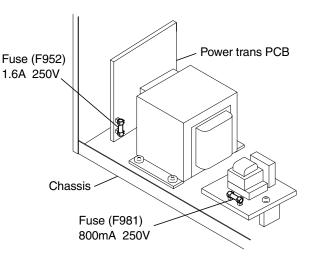


Fig.1

Replacing the power IC

(See Fig.2 and 3)

- Prior to performing the following procedure, remove the top cover.
- Also remove the rear panel. (See Fig.33)
- 1. Remove the screw "A" from the bracket that retains the power IC.
- 2. Remove the solder fixing the power IC.

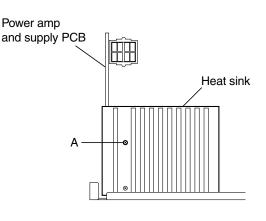
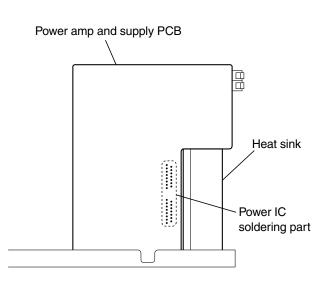


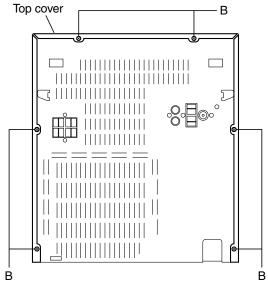
Fig.2



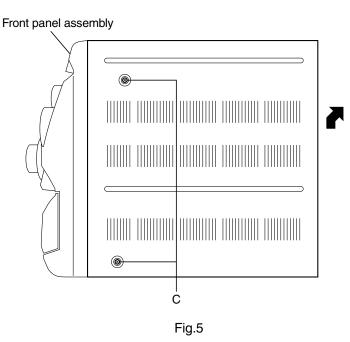
Removing the top cover

(See Fig.4 and 5)

- 1. Remove the six screws "B" that retain the top cover from the rear of the body.
- 2. Remove the four screws "C" that retain the top cover from the two sides of the body.
- 3. Remove the top cover from the body by lifting it toward the rear.

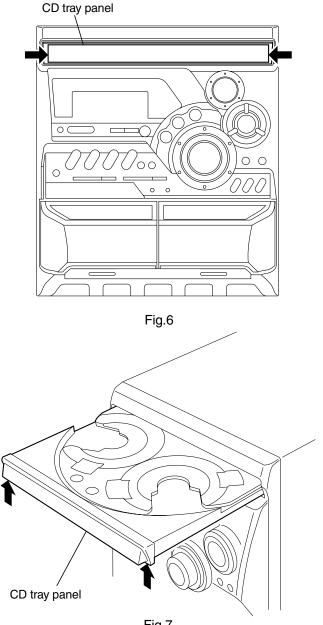




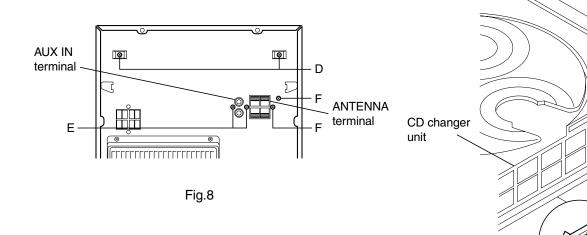


Removing the CD changer unit (See Fig.6 to 9)

- Prior to performing the following procedures, remove the top cover.
- [Caution] Although the CD mechanism unit can be removed without removing the CD tray panel, it is still recommended to remove it in order to prevent damage.
- a. From the front panel side of this set, push in the sections marked with arrows and pull out the CD tray toward the front.
- b. Remove the CD tray panel by pushing both of its extremities upward in the direction of the arrows.
- c. Push the CD tray deep into the set.
- 1. Disconnect the cassette head wires from the connectors CN307 and CN308 on the main PCB, which is located below the CD changer unit.
- 2. Disconnect the card wires from the connectors CN301, CN302 and CN305 on the main PCB.
- 3. Disconnect the parallel wires from the connectors CN902A, CN902B and CN902C on the power amp and supply PCB.
- 4. From the rear of the set, remove the two screws "D" retaining the CD changer unit, then remove the two screws "E" and the two screws "F" retaining the ANTENNA terminal and the AUX IN terminal.
- 5. Push down and disengage the two claws "a" holding the CD changer unit at the bottom of its front panel and then remove the CD changer unit (Fig. 9).



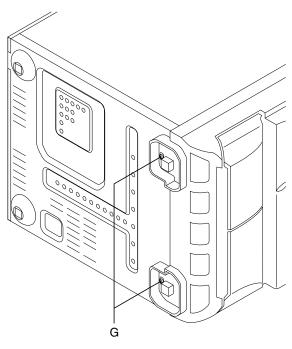




Claw a

Removing the front panel assembly (See Fig.10 and 11)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- 1. Disconnect the parallel wire and card wire from the connectors CN101 and CN901 on the power amp and supply PCB.
- 2. Remove the two screws "G" retaining the front panel assembly onto the bottom of the body.
- 3. Remove the screw "H" retaining the cassette deck mechanism and then remove the GND lug "b" that comes from the power amp and supply PCB.
- 4. Remove the solder connecting the wire on the control/FL PCB.
- 5. Disengage the claws "c" on both sides at the bottom of the front panel assembly and then remove the assembly.





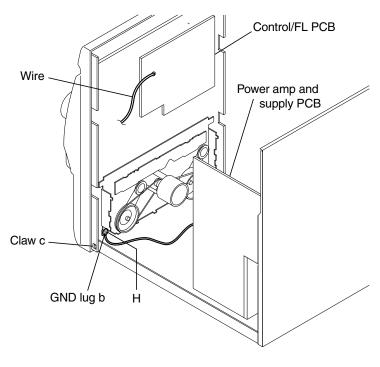


Fig.11

<Disassembly of units and assembly inside this set>

Removing the main PCB (See Fig.12 and 13)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- 1. Disconnect the wires from CN602 and CN603 on the main PCB, which is located on the backside of the CD changer unit.
- 2. Disconnect the card wire from connector CN604 of the main PCB at the back of the CD changer unit.
- 3. The three screws "I" and the screw with the washer "J" that retain the main PCB should be removed.
- 4. Remove the main PCB by pulling it toward the side where the CN601 is located.
- 5. Using solder, short the CD pickup to connect to short round.
- **[Caution]** After re-connecting the wires, be sure to remove the shorting solder from the GND connection.
- 6. Disconnect the card wire from the connector CN601 on the main PCB and then remove the main PCB.

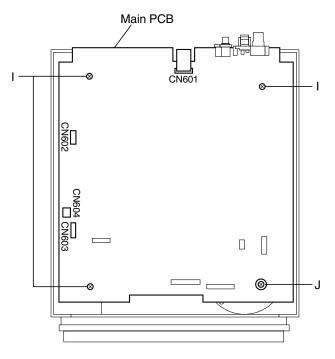
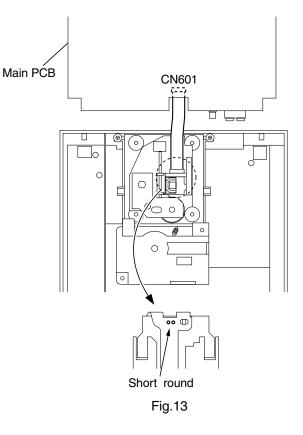
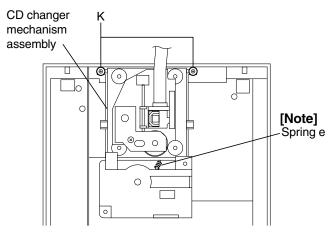


Fig.12

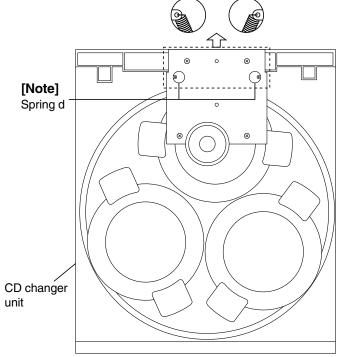


Removing the CD changer mechanism assembly (See Fig.14 to 16)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- 1. Remove the spring "d" from the front surface of the CD changer mechanism unit.
- 2. Turn the CD changer mechanism unit upside down, and remove the spring "e" connecting the unit to the CD changer mechanism assembly.
- [Note] When re-assembling after the disassembly work, apply bonding agent on the spring to prevent it from slipping off.
- 3. Remove the two screws "K" retaining the CD changer mechanism assembly.
- [Caution] When replacing the CD changer mechanism assembly, be sure not to mistake the positions of the red and blue dampers.









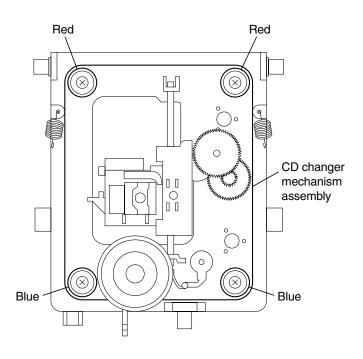


Fig.16

Removing the CD pickup (See Fig.17)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- Also remove the CD changer mechanism
- 1. Widen the section "f ".
- 2. While keeping the section "f" wide open, push the section "g" in the direction of the arrow to remove the shaft, and then remove the CD pickup.

Replacing the loading motor and belt of the CD changer tray (See Fig.18)

- Prior to performing the following procedure, remove the top cover.
- 1. Pull out the CD changer tray.
- 2. Remove the belt from the pulley.
- 3. Remove the two screws "L" retaining the CD changer tray loading motor.
- **[Caution]** When pushing in the CD changer tray, be sure to lower the CD changer mechanism assembly by manually turning the UD cam gear in the direction of the arrow.

Replacing the CD tray rotor belt of the CD changer, and removing the motor (See Fig.19 and 20)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- 1. Remove the two screws "M" retaining the stopper brackets on both sides of the CD changer unit.
- 2. Remove the stopper brackets from both sides of the CD changer unit.
- 3. Pull out the CD tray from the CD changer unit, all the way and then lift the tray upward to remove.
- 4. Turn the CD tray upside down and remove the tray rotor belt from the pulley.
- 5. Turn the CD tray upside down and disconnect the card wire from the connector CN605 on the CD tray rotor motor assembly.
- 6. Remove the two screws "N" retaining the CD tray rotor motor, and then remove the CD tray rotor motor.

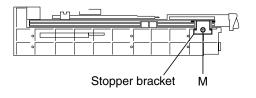
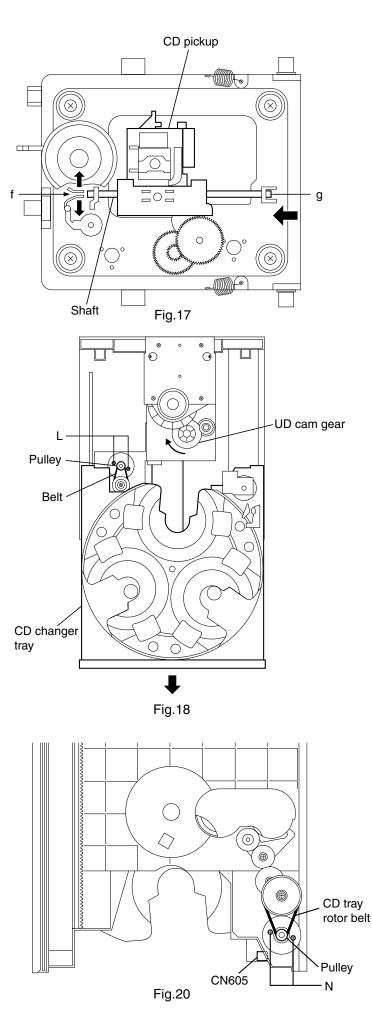


Fig.19



Front panel

assembly

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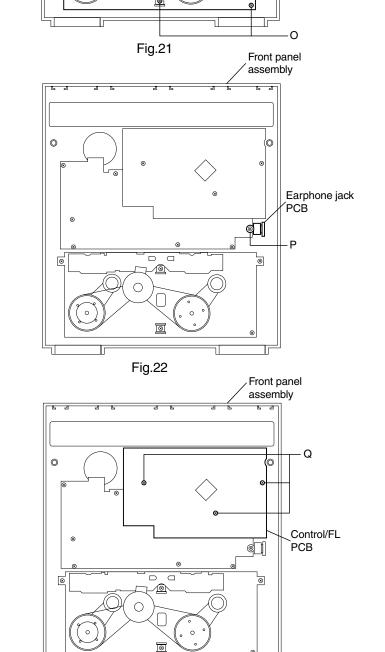
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Removing the cassette deck mechanism (See Fig.21)

• Prior to performing the following procedure, remove the top cover.

O

- Also remove the CD changer unit.
- Also remove the front panel assembly.
- 1. Remove the five screws "O" retaining the cassette deck mechanism from the back of the front panel assembly.



Removing the earphone jack PCB (See Fig.22)

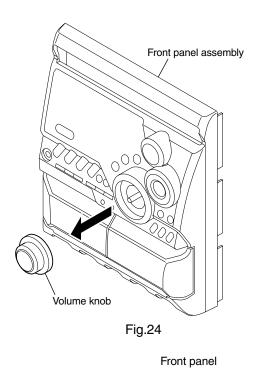
- Prior to performing the following procedure, remove the top cover
- Also remove the CD changer unit.
- Also remove the front panel assembly.
- 1. Remove the screw with the washer, "P" that retains the earphone jack PCB from the back of the front panel assembly.

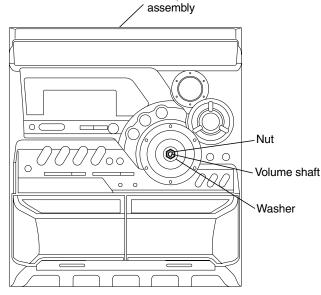
Removing the control/FL PCB (See Fig.23)

- Prior to performing the following procedure, remove the top cover.
- Also remove the CD changer unit.
- 1. Remove the three screws "Q" that retain the control/FL PCB from the back of the front panel assembly.

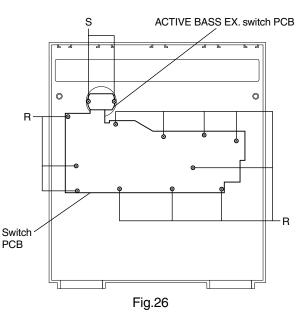
Removing the switch PCB and ACTIVE BASS EX. switch PCB (See Fig.23 to 26)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- Also remove the front panel assembly.
- 1. Pull out the volume control knob from the front of the front panel assembly.
- 2. Remove the nut and washer retaining the volume knob shaft.
- 3. Remove the three screws "Q" retaining the control/FL PCB from the back of the front panel assembly.
- 4. Remove the control/FL PCB.
- 5. Remove the eleven screws "R" retaining the switch PCB.
- 6. Remove the two screws "S" retaining the ACTIVE BASS EX. switch PCB.









Removing the cassette deck main motor, and replacing the main belts (See Fig.21, 27 and 28)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- Also remove the front panel assembly.
- 1. Remove the five screws "O" retaining the cassette deck mechanism.
- 2. Remove the cassette deck mechanism.
- 3. Remove the two screws "T" retaining the main motor from the front side of the cassette deck.
- **[Caution]** After attaching the main motor, check the orientation of the motor and the polarity of the wires.
- 4. From the backside of the cassette deck, remove the main motor and two main belts.
- [Caution] The lengths of the cassette A (playback only) and cassette B (record/play) main belts are different. When attaching the main belts, use the longer belt for cassette A.

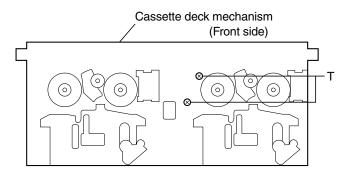


Fig.27

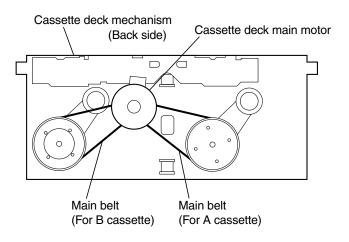


Fig.28

Removing the leaf switches of the cassette deck mechanism (See Fig.21 and 29)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- Also remove the front panel assembly.
- 1. Remove the five screws "O" that retain the cassette deck mechanism.
- 2. Remove the cassette deck mechanism.
- 3. Turn the cassette deck mechanism upside down.
- 4. Remove the solder from around the leaf switches.
- 5. Pull out the leaf switches from the front side of the cassette deck mechanism.

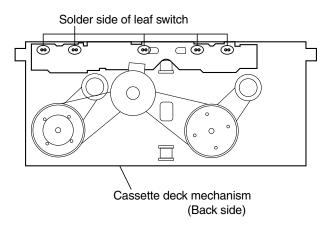


Fig.29

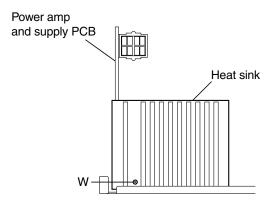
Removing the cassette deck heads (See Fig.21 and 30)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- Also remove the front panel assembly.
- 1. Remove the five screws "O" that retain the cassette deck mechanism.
- 2. Remove the cassette deck mechanism and place it so that the front side faces up.
- 3. Remove the solder from the bottom side of the head terminal and disconnect the wire.
- 4. Remove the screw "U" that retains the head.
- 5. Loosen the screw "V" that retains the head.

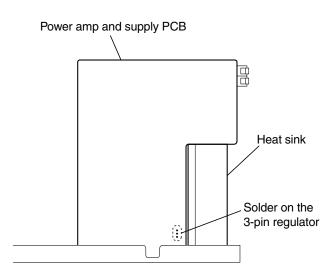
Removing the 3-pin regulator

6. Hold the head and slide it in the direction of the arrow to remove it.

Cassette deck mechanism (Front side) PB Head PB Head Fig.30







Prior to performing the following procedures, remove the top cover. Also remove the rear panel. (See Fig.33)

(See Fig.31 and 32)

- 1. Remove the screw "W" that retains the bracket holding the 3-pin terminal regulator.
- 2. Remove the solder fixing the 3-pin regulator.

Removing the power amp and supply PCB and the power trans PCB

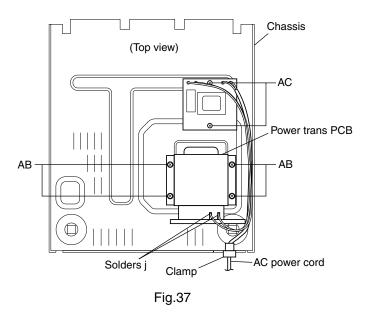
(See Fig.33 to 37)

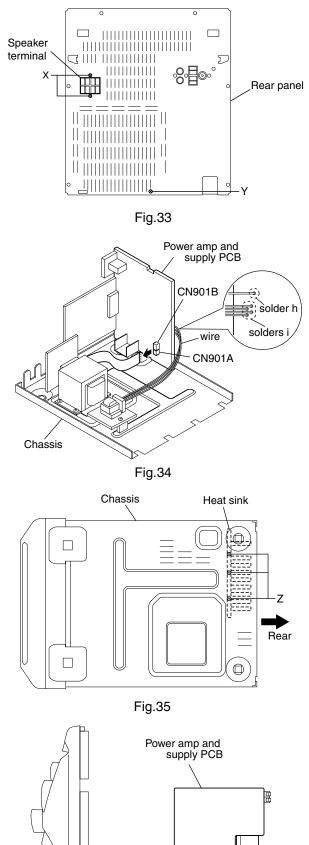
- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- 1. Remove the two screws "X" that retain the SPEAKERS terminals.
- 2. Remove the screw "Y" that retains the rear panel, and then remove the rear panel.
- 3. Disconnect the parallel wires from the connectors CN901A and CN901B on the power amp and supply PCB.
- 4. Disconnect the parallel wire and card wire from the connectors CN101 and CN901 on the power amp and supply PCB.
- 5. Remove the solders "h" and "i" connecting the five wires.
- 6. Remove the three screws "Z" retaining the heat sink onto the bottom of the chassis.
- 7. Remove the screw "AA" that retain the power amp and supply PCB and then remove the assembly.
- 8. Remove the solders "j" connecting the two wires.
- 9. Remove the four screws "AB" that retain the power trans PCB and then remove the assembly.

Removing the sub power PCB

(See Fig.37)

- Prior to performing the following procedures, remove the top cover.
- Also remove the CD changer unit.
- Also remove the rear panel. (See Fig.33)
- 1. Remove the solders "j" connecting the two wires.
- 2. Remove the clamp of AC power cord from the chassis.
- 3. Remove the fwo screws "AC" that retain the sub power PCB and then remove the assembly.





Chassis

Fig.36

AA

Adjustment method

Measurement instruments required for adjustment

- Low frequency oscillator, This oscillator should have a capacity to output 0dB to 600ohm at an oscillation frequency of 50Hz-20kHz.
- 2. Attenuator impedance : 600ohm
- 3. Electronic voltmeter
- 4. Frequency counter
- 5. Wow flutter meter
- 6. Test tape

VT712 : For Tape speed and wow flutter (3kHz)

- VT703 : For Head angle(10kHz),Play back frequency characteristics(1kHz),and dubbing frequency characteristics(63,1,10kHz)
- 7. Blank tape
- TAPE I : AC-225, TAPE II : AC-514
- 8. Torque gauge : For play and back tension Forward ; TW2111A, Reverse ; TW2121A Fast Forward and Rewind ; TW2231A
- 9. Test disc
 - : CTS-1000(12cm),GRG-1211(8cm)
- 10. Jitter meter

Measurement conditions

Power supply voltage AC230V (50Hz)

Measurement

output terminal : Speaker out

- : TP101(Measuring for TUNER/DECK/CD)
- : Dummy load 6ohm

Radio input signal

AM modulation frequency : 400Hz Modulation factor : 30% FM modulation frequency : 400Hz Frequency displacement : 22.5kHz

Frequency Range

 FM
 87.5MHz~108MHz

 MW
 522kHz~1629kHz

 LW
 144kHz~228kHz

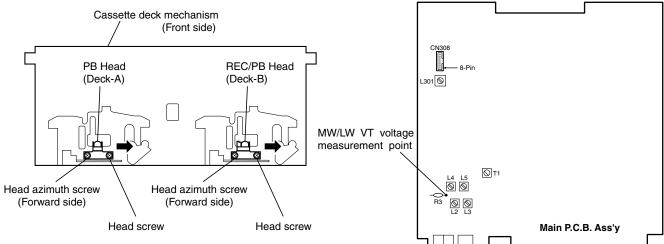
Standard measurement positions of volume and switch

Power : Standby (Light STANDBY Indicator) S,A,BASS : OFF Sound mode : OFF Main VOL. : 0 Minimum Traverse mecha set position : Disc 1

Precautions for measurement

- 1. Apply 30pF and 33kohm to the IF sweeper output side and 0.082μ F and 100kohm in series to the sweeper input side.
- 2. The IF sweeper output level should be made as low as possible within the adjustable range.
- 3. Since the IF sweeper is a fixed device, there is no need to adjust this sweeper.
- 4. Since a ceramic oscillator is used, there is no need to perform any MPX adjustment.
- 5. Since a fixed coil is used, there is no need to adjust the FM tracking.
- 6. The input and output earth systems are separated. In case of simultaneously measuring the voltage in both of the input and output systems with an electronic voltmeter for two channels, therefore, the earth should be connected particularly.
- 7. In the case of BTL connection amplifier, the minus terminal of speaker is not for earthing. Therefore, be sure not to connect any other earth terminal to this terminal. This system is of an OTL system.

■Arrangement of adjusting positions



■ Tape recorder section

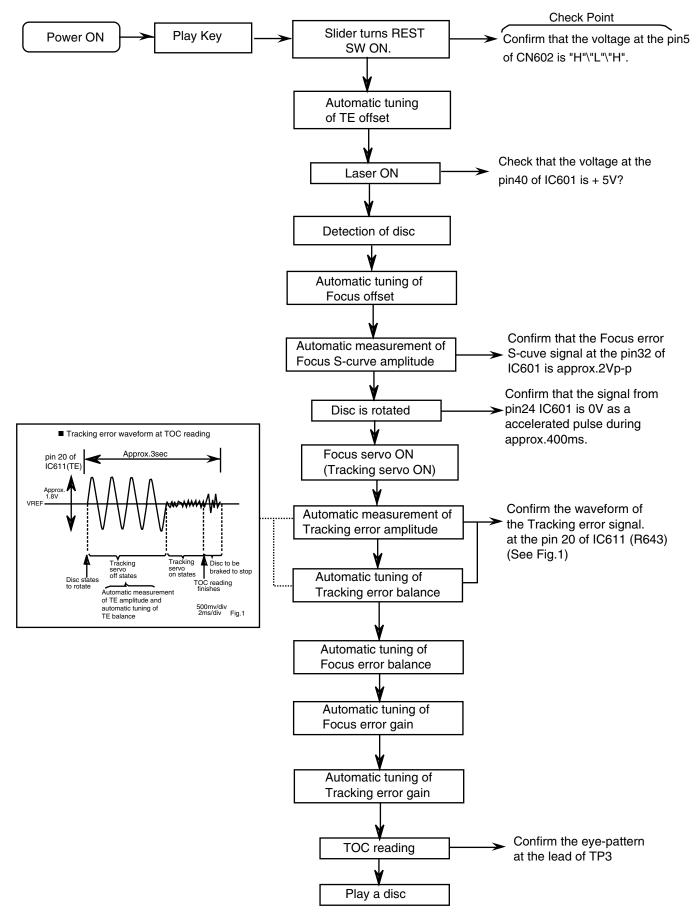
| Items | Measurement conditions | Measurement method | Standard values | Adjusting positions |
|---------------------------------------|---|--|--------------------|--|
| Cassette Head Azimuth Alignments | Test tape : VT703 (10kHz) Measurement output terminal : Left and Right | Playback the test tape VT703 (10KHz) or equivalent. Adjust the head azimuth screw to obtain maximum output and both output of L / R is in 3dB. Put on the screw lock paint after alignments. | Maximum output | Head azimuth screw |
| | speaker output (6-ohm loaded) or Headphone Output (32-ohm loaded) | | | Adjust the head azimuth screw only when the head has been changed. |
| Recording Bias Frequency Alignment | Test tape : TYPE II AC-514 Measurement output terminal : Erase head terminal (CN308 8-Pin) | Insert the recording tape in deck-B. Starting the recording. Adjust the oscillation frequency to 80KHz+/-3KHz by core of Oscillation coil of L301. | 80kHz+/-3kHz | Bias coil: L301 Use the High- Impedance Probe for Frequency counter input. |

Tuner section

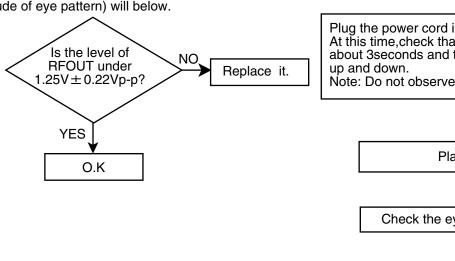
| Items | Measurement conditions | Measurement method | Standard values | Adjusting positions |
|---------------------------|--|--|--|--|
| AM Tracking Alignments | Input signal : 529kHz (530kHz) 603kHz (600kHz) Measurement point : Resistor (R3) terminal | Set the Signal Generator signal to 529KHz (530KHz) the feed to Loop Antenna. Receiving the signal and the adjust the OSC coil L4(MW)/L5(LW) obtain the V.T is 1.40V +/-0.05V. Change the receiving frequency to 603KHz (600KHz). Adjust the Antenna coil L2(MW)/L3(LW) obtain maximum sensitivity. (Adjust the SSG output to out of AGC range.) | V.T : 1.40V+/-0.05V Maximum sensitivity | OSC coil :L4(MW)/L5(LW) Antenna coil :L2(MW)/L3(LW) Adjust the OSC coil only when the AM coil block has been changed. |
| AM IFT Alignments | Input signal : 530kHz (529kHz) | Set the receiving frequency to 529KHz (530KHz) Feed the 450KHz signal to AM antenna input. Adjust the IFT Block T1 obtain to maximum output. (Adjust the SSG output to out of AGC range.) | Maximum output | IFT(T1) Adjust the IFT only when the IFT block has been changed. |

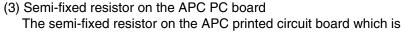
Note: The adjustment of CD section is not required.

Flow of functional operation until TOC read



- (1) Cleaning the pick up lens Before you replace the pick up, please try to clean the lens with a alcohol soaked cotton swab.
- (2) Life of the laser diode When the life of the laser diode has expired, the following symptoms will appear.
 - 1. The level of RF output (EFM output:amplitude of eye pattern) will below.



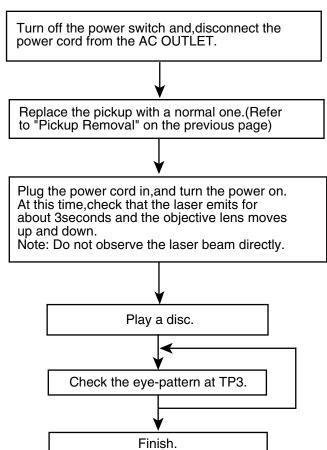


attached to the pickup is used to adjust the laser power. Since this adjustment should be performed to match the characteristics of the whole optical block, do not touch the semi-fixed resistor.

If the laser power is lower than the specified value, the laser diode is almost worn out, and the laser pickup should be replaced.

If the semi-fixed resistor would be adjusted when the pickup operates normally, the laser pickup may be damaged due to excessive current.

Maintenance of laser pickup Replacement of laser pickup

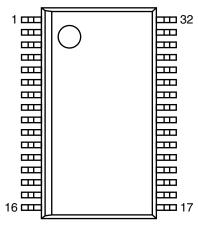


Description of major ICs

AN22000A (IC611) : CD-DA Head Amp.

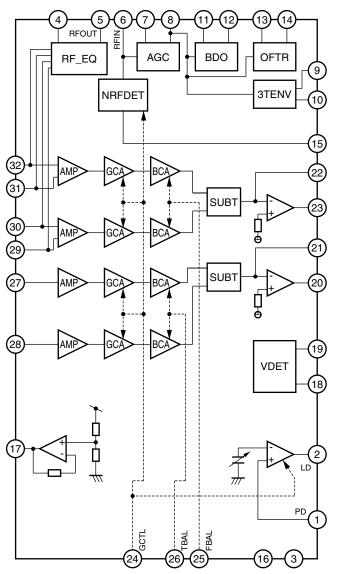
1. Terminal layout

2. Pin function



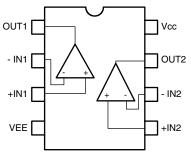
| Pin No. | Symbol | I/O | Function | Pin No. | Symbol | I/O | Function |
|------------|--------|-----|-------------------------------|------------|--------|-----|----------------------------------|
| 1 | PD | 1 | APC Amp. input terminal | 15 | NRFDET | 0 | NRFDET output terminal |
| 2 | LD | 0 | APC Amp. output terminal | 16 | GND | - | Ground |
| 3 | VCC | - | Power supply terminal | 17 | VREF | 0 | VREF output terminal |
| 4 | RFN | 1 | RF adder Amp. inverting input | 18 | VDET | 0 | VDET output terminal |
| | | | terminal | 19 | TEBPF | Ι | VDET output terminal |
| 5 | RFOUT | 0 | RF adder Amp. output terminal | 20 | TEOUT | 0 | TE Amp. output terminal |
| 6 | RFIN | Ι | AGC input terminal | 21 | TEN | Ι | TE Amp. inverting input |
| 7 | CAGC | Ι | Input terminal for AGC loop | 1 | | | terminal |
| | | | filter capacitor | 22 | FEN | Ι | FE Amp. inverting input |
| 8 | ARF | 0 | AGC output terminal | 1 | | | terminal |
| 9 | CBA | Ι | Capacitor connecting terminal | 23 | FEOUT | 0 | FE Amp. output terminal |
| | | | for HPF-Amp. | 24 | GCTL | 0 | GCTL & APC terminal |
| 10 | 3TOUT | 0 | 3 TENV output terminal | 25 | FBAL | 0 | FBAL control terminal |
| 11 | CBOO | Ι | Capacitor connecting terminal | 26 | TBAL | 0 | TBAL control terminal |
| | | | for envelope detection on the | 27 | E | Ι | Tracking signal input terminal 1 |
| | | | darkness side | 28 | F | Ι | Tracking signal input terminal 2 |
| 12 | BDO | 0 | BDO output terminal | 29 | D | Ι | Focus signal input terminal 4 |
| 13 | COFTR | Ι | Capacitor connecting terminal | 30 | В | | Focus signal input terminal 3 |
| | | | for envelope detection on the | 31 | С | | Focus signal input terminal 2 |
| | | | light side | 32 | A | Ι | Focus signal input terminal 1 |
| 14 | OFTR | 0 | OFTR output terminal | | | | |

3. Block diagram



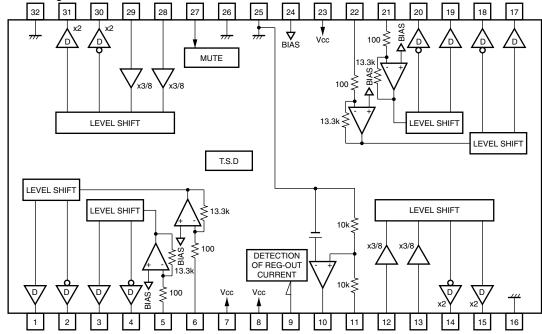
■ BA15218 (IC102) : Dual low noise operational amp.

1. Terminal layout



BA5936S (IC621) : 6 channel BTL driver

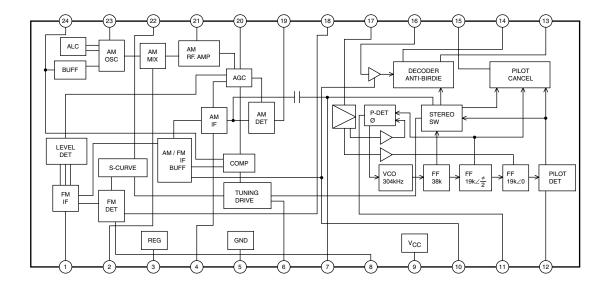
1. Block diagram



| Pin No. | Symbol | I/O | Function | Pin No. | Symbol | I/O | Function | |
|------------|---------|-----|---|------------|---------|-----|-----------------------|--|
| 1 | OUT1- | 0 | CH1 negative output | 17 | OUT4- | 0 | CH4 negative output | |
| 2 | OUT1+ | 0 | CH1 positive output | 18 | OUT4+ | 0 | CH4 positive output | |
| 3 | OUT2- | 0 | CH2 negative output | 19 | OUT5- | 0 | CH5 negative output | |
| 4 | OUT2+ | 0 | CH2 positive output | 20 | OUT5+ | 0 | CH5 positive output | |
| 5 | IN2 | Ι | CH2 input | 21 | IN5 | Ι | CH5 input | |
| 6 | IN1 | Ι | CH1 input | 22 | IN4 | Ι | CH4 input | |
| 7 | VCC | - | Power supply terminal | 23 | VCC | - | Power supply terminal | |
| 8 | VCC | - | Power supply terminal | 24 | BIAS IN | Ι | Bias input terminal | |
| 9 | REG-I | Ι | Regulator current detection terminal | 25 | GND | - | Ground | |
| 10 | REG-B | 0 | Connect to base terminal of external transistor | 26 | GND | - | Ground | |
| 11 | REG-OUT | 0 | Constant voltage output (Connect to | 27 | MUTE IN | I | Mute input terminal | |
| | | | collector terminal of external transistor) | | | | | |
| 12 | IN3-R | Ι | CH3 reverse input | 28 | IN6-R | Ι | CH6 reverse input | |
| 13 | IN3-F | I | CH3 forward input | 29 | IN6-F | I | CH6 forward input | |
| 14 | OUT3+ | 0 | CH3 positive output | 30 | OUT6+ | 0 | CH6 positive output | |
| 15 | OUT3- | 0 | CH3 negative output | 31 | OUT6- | 0 | CH6 negative output | |
| 16 | GND | - | Ground | 32 | GND | - | Ground | |

■ LA1833 (IC1) : 1chip AM/FM, MPX tuner system

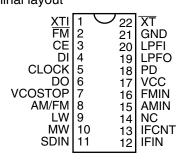
1. Block diagram



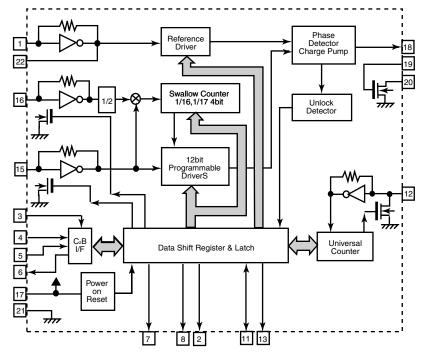
| Pin No. | Symbol | I/O | Function | Pin No. | Symbol | I/O | Function |
|------------|------------|-----|---|------------|--------------|-----|------------------------------|
| 1 | FM.IF IN | Ι | Input of FMIF signal | 13 | L.OUT | 0 | Output L-channel |
| 2 | AM.MIXOUT | Ι | Output terminal for AM mixer | 14 | R.OUT | 0 | Output R-channel |
| 3 | REG | | | 15 | P.CANCEL | 0 | Pilot cancel output |
| 4 | AM.IF IN | Ι | Input of AMIF signal | 16 | DECODE.IN | Ι | Decoder input |
| 5 | GND | - | Ground | 17 | MPX.IN | Ι | PLL input |
| 6 | TU.OUT | - | Not use | 18 | FM.OUT | 0 | FM demodulator output |
| 7 | ST.OUT | 0 | IF output for AM stereo | 19 | AM.OUT | 0 | AM detection output |
| 8 | FM.DET | 0 | FM detection signal output | 20 | AGC | Ι | S meter, AM AGC |
| 9 | VCC | - | Power supply terminal | 21 | AM.RFIN | Ι | AMRF signal input |
| 10 | IF.OUT | I | AM/FM IF count output | 22 | AFC | | |
| 11 | LPF1/BAND | Ι | Phase comparator filter pin, AM/FM select | 23 | AM.OSC | - | AM local oscillation circuit |
| 12 | LPF2/MO/ST | I | Pilot detect filter pin, mono, VCO stop | 24 | FM/SD/OSCOUT | 0 | OSC buffer, FM Adj. |

LC72136N (IC2) : PLL Frequency synthesizer

1.Terminal layout



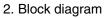
2. Block diagram

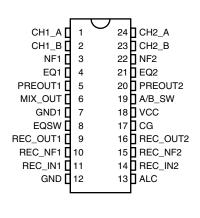


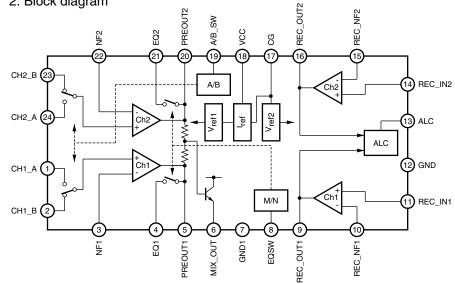
| Pin No. | Symbol | I/O | Function | Pin No. | Symbol | I/O | Function |
|------------|---------|-----|--|------------|--------|-----|--|
| 1 | XTI | Ι | X'tal oscillator connect (75KHz) | 12 | IFIN | Ι | IF counter signal input |
| 2 | FM | 0 | LOW:FM mode | 13 | IFCNT | 0 | IF signal output |
| 3 | CE | Ι | When data output/input for 4pin(input) and | 14 | NC | - | Not use |
| | | | 6pin(output): H | | | | |
| 4 | DI | Ι | Input for receive the serial data from | 15 | AMIN | Т | AM Local OSC signal output |
| | | | controller | | | | |
| 5 | CLOCK | Ι | Sync signal input use | 16 | FMIN | Ι | FM Local OSC signal input |
| 6 | DO | 0 | Data output for Controller | 17 | VCC | - | Power supply(VDD=4.5~ 5.5V) |
| | | | Output port | | | | When power ON:Reset circuit move |
| 7 | VCOSTOP | 0 | "Low": MW mode | 18 | PD | 0 | PLL charge pump output(H: Local OSC |
| | | | | | | | frequency Height than Reference frequency. |
| | | | | | | | L: Low Agreement: Height impedance) |
| 8 | AM/FM | 0 | Open state after the power on reset | 19 | LPFO | 0 | Output for active lowpassfilter of PLL |
| 9 | NC | - | Input/output port | 20 | LPFI | Ι | Input for active lowpassfilter of PLL |
| 10 | NC | - | Input/output port | 21 | GND | - | Connected to GND |
| 11 | SDIN | I/O | Data input/output | 22 | XT | Ι | X'tal oscillator(75KHz) |

TA8189N (IC401) : REC/PB amp.

1. Terminal layout



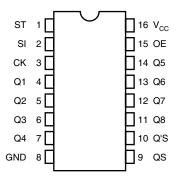




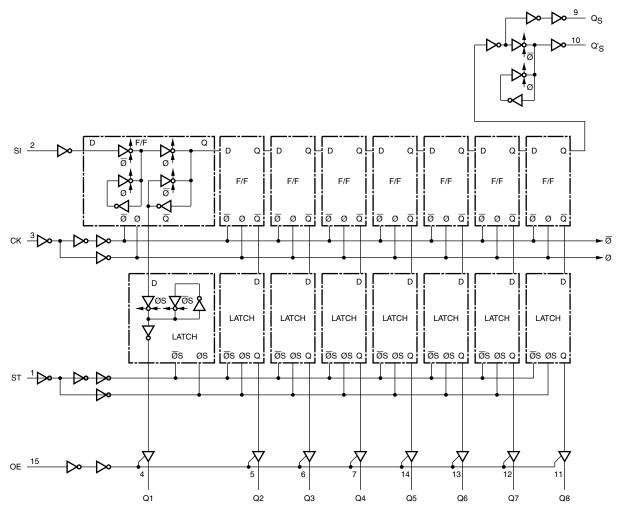
| Pin No. | Symbol | I/O | Function | Pin No. | Symbol | I/O | Function |
|------------|----------|-----|---|------------|----------|-----|---|
| 1 | CH1_A | Ι | Input for playback amp. (Ch1): A cassette | 13 | ALC | Ι | ALC filter terminal |
| | | | (PB) | | | | |
| 2 | CH1_B | Ι | Input for playback amp. (Ch1): B cassette | 14 | REC_IN2 | Ι | Input for recording amp. |
| | | | (REC/PB) | | | | |
| 3 | NF1 | Ι | NF terminal for playback amp. | 15 | REC_NF2 | Т | NF terminal for recording amp. |
| 4 | EQ1 | 0 | Metal output for playback amp. | 16 | REC_OUT2 | 0 | Output for recording amp. |
| 5 | PREOUT1 | 0 | Output terminal for playback amp. | 17 | CG | 0 | Charge detection terminal |
| 6 | MIX_OUT | 0 | Mixing output | 18 | VCC | - | Power supply terminal |
| 7 | GND1 | - | Ground | 19 | A/B_SW | Ι | REC amp. Select switch (A cassette/B |
| | | | | | | | cassette) |
| 8 | EQSW | Ι | Metal/Normal mode select switch | 20 | PREOUT2 | 0 | Output terminal for playback amp. |
| 9 | REC_OUT1 | 0 | Output for recording amp. | 21 | EQ2 | 0 | Metal output for playback amp. |
| 10 | REC_NF1 | Ι | NF terminal for recording amp. | 22 | NF2 | Ι | NF terminal for playback amp. |
| 11 | REC_IN1 | Ι | Input for recording amp. | 23 | CH2_B | Ι | Input for playback amp. (Ch2): B cassette |
| | | | | | | | (REC/PB) |
| 12 | GND1 | - | Ground terminal | 24 | CH2_A | Ι | Input for playback amp. (Ch2): A cassette |
| | | | | | | | (PB) |

TC74HC4094AP (IC402) : 8-bit shift and store resister

1. Terminal layout

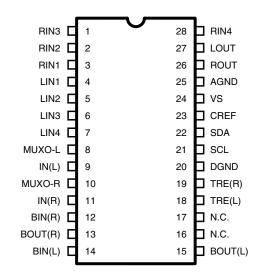


2. Block diagram

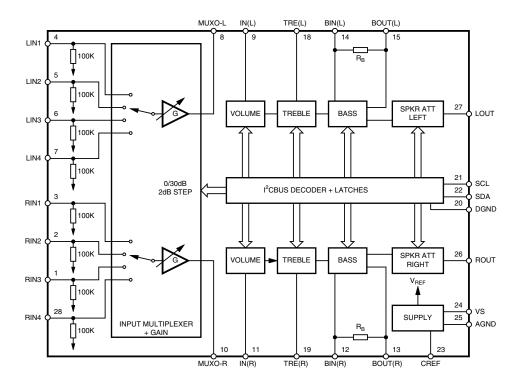


■ TDA7440D (IC101) : Audio processor

1. Terminal layout

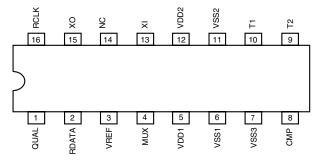


2. Block diagram

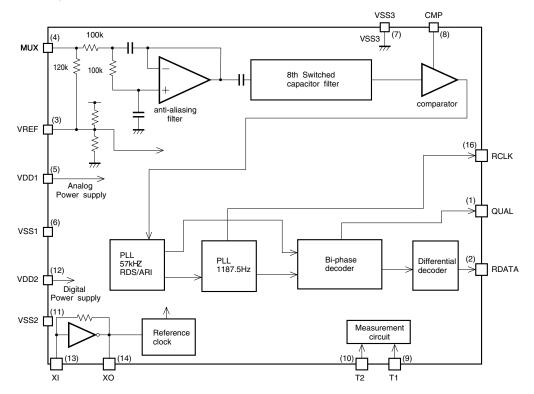


BU1923 (IC3) : RDS/RBDS decoder

1. Terminal layout



2. Block diagram



| Pin No. | Symbol | I/O | Function | Pin No. | Symbol | I/O | Function | |
|------------|--------|-----|---------------------------------|------------|--------|-----|-------------------------------------|--|
| 1 | QUAL | 0 | Demodulation quality, | 9 | T1 | Ι | Input for test | |
| | | | H: Proper data L: Improper data | | | | | |
| 2 | RDATA | 0 | Demodulation data | 10 | T2 | I | Input for test | |
| 3 | VREF | Т | Reference power supply 1/2VDD1 | 11 | VSS2 | - | Digital power supply | |
| 4 | MUX | I | Composite signal input | 12 | VDD2 | - | Digital power supply | |
| 5 | VDD1 | - | Analog power supply | 13 | XI | I | Crystal oscilltor(4.332MHz) connect | |
| 6 | VSS1 | - | Analog power supply | 14 | хо | 0 | Crystal oscilltor(4.332MHz) connect | |
| 7 | VSS3 | - | GND | 15 | NC | - | Not use | |
| 8 | CMP | Ι | Comparator input | 16 | RCLK | 0 | Demodulation clock | |

MX-K1R

MN662748RPM (IC601) : Digital servo & digital signal processor

1. Terminal layout

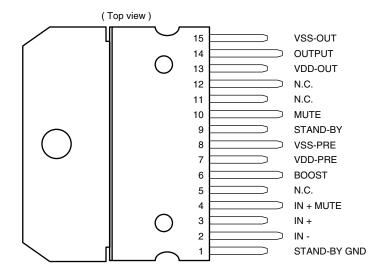
| | , | |
|----|-------|----|
| • | 80~61 | |
| 1 | 6 | 50 |
| ł | | 2 |
| 20 | 2 | 11 |
| | 21~40 | |

| Bit Dick Construction Distribution Distribution 2 LRCK - Not use 41 PLF2 - Not use 2 LRCK - Not use 42 TOPS - Not use 3 SRDATA - Not use 43 WVEL - Not use 4 DVDD1 - Power supply for digital circuit 44 INF 1 Reference current input 6 TX - Not use 6 TX - Not use 6 MATA 1 Micro computer command 48 PLCF //O Loop filter pin for PSL 7 MCLK 1 Micro computer command 9 //CoP Not use, connect to TP5 52 POWEr Not use, connect to TP12 1 FLOCK - Not use, connect to TP5 54 VCO2 //O Loop filter pin for Digital serve VCO 13 BLKCK 0 State code Q register input 57 | Pin No | Symbol | 1/0 | Function | Pin No | Symbol | I/O | Function |
|--|-----------|--------|-----|----------------------------------|----------------|--------|----------|-------------------------------|
| IDCK Not use 42 TOFS - Not use 3 SRDATA Not use 43 NVEL Not use 44 4 DVDDI - Power supply for digital circuit 44 ARF 1 Reference current input 5 DVSSI - Not use 46 DRF 1 Blas pin for DSL 7 MCLK I Micro computer command 47 DSLF I/O Loop filter pin for PLL 8 MDATA I Micro computer command 49 VCOF I/O Loop filter pin for PLL 8 MDATA I Micro computer command 40 OADD2 - Power supply for analog circuit 10 SENSE Not use, connect to TP5 54 VCOF2 I/O Loop filter pin for D121 11 11 FLOCK - Not use, connect to TP5 54 VCOF2 I/O Loop filter pin for Digital signal input 13 BLKCK O Sub code Diok clock Sub code Code Lock Sub code Code Lock Sub code Sub | | | | | | | | |
| 3 SRDATA - Not use 43 WVEL - Not use 4 DVDDI - Power supply for digital circuit 44 APF 1 Resignal input 5 DVSSI - INIC use 46 DPF 1 Blas pin for DSL 6 TX - Not use 46 DPF 1 Blas pin for DSL 7 MCLK I Micro computer command 48 PLLF I/D Loop filter pin for YCL 8 MATA I Nicro computer command 49 VCOF I/D Loop filter pin for YCD 9 MLD I Not use, connect to TP7 52 EFM - Not use, connect to TP12 11 FLOCK - Not use, connect to TP5 54 VCOF2 I/D Loop filter pin for Digital serve 13 BLRCK O Sub code Lobot clock 5 SUE - Not use 13 BLRCK O Sub code Lobot clock 5 SUE - Not use | | | | | | | | |
| 4 DVDI - Power supply for digital circuit 44 AFF I Reference current input 6 TX - Not use 45 IREF I Reference current input 6 TX - Not use 46 DRF I Blas pin for DSL 7 MCLK I Micro computer command 49 VCOF I/O Loog filter pin for DSL 8 MDATA I Micro computer command 49 VCOF I/O Loog filter pin for VCO 9 MLD I Micro computer command 51 AVSS2 GND for analog circuit 10 SENSE - Not use, connect to TP7 52 EFM - Not use, connect to TP12 12 TLOCK - Not use, connect to TP5 53 PCK O Clock output for PL1 12 TLOCK - Not use, connect to TP3 55 SUBC Not use 13 BLKCK O Sub code D data output 57 | | | | | | | | |
| 5 DVSSI - GND for digital circuit 45 IREF I Reference current input 6 TX - Not use 46 DPF I Bias pin for DSL 7 MCLK I Micro computer command clock signal input 47 DSLF I/O Loop filter pin for DSL 8 MDATA I Micro computer command data input 48 PLF I/O Loop filter pin for DSL 9 MLD I Micro computer command load signal input (L: Load) 51 AVSS2 GND for analog circuit 10 SENSE - Not use, connect to TP6 53 PCK O Clock output for PLL 12 TLOCK - Not use, connect to TP6 54 VCOF2 I/O Loop filter pin for PDI 13 BLKCK O Sub code block clock 55 SUBC - Not use serve VCO 14 SOCK I External clock input for sub signal output 56 SBCK Not use contact 15 SUBQ O Stub code d at output 57 VSS GND for crystal oscillation circuit (=16.9344MH2) 17 TSTAT O Stats | _ | | | | | | | |
| 6 TX - Not use 46 DRF I Bias pin for DSL. 7 MCLK I Micro computer command data input 47 DSLF I/O Loop filter pin for PSL. 8 MDATA I Micro computer command data input 49 VCOF I/O Loop filter pin for VCO. 9 MLD I Micro computer command to ad signal input 6107 AVDD2 - Power supply for analog circuit 10 SENSE - Not use, connect to TP5 52 FFM - Not use, connect to TP1 11 FLOCK - Not use, connect to TP5 54 VCOF2 I/O Loop filter pin for Digital serve VCO 13 BLKCK O Sub code D data output 75 VSS - GND for crystal oscillation circuit - Inot use 15 SUBQ O Status signal input 58 X1 I input for crystal oscillation circuit (=16.9344MHz) 19 SMCK - Not use O Taverse drive cutput 61 BYTCK/TRVSTP Not use 19 SMCK - Not used < | _ | | | Power supply for digital circuit | | | | |
| 7 MCLK I Micro computer command clock signal input 47 DSLF I/O Loop filter pin for DSL loop filter pin for DSL 8 MDATA I Micro computer command data input 49 VCOF I/O Loop filter pin for VCO 9 MLD I Micro computer command load signal input (L: Load) 51 AVSS2 GND for analog circuit 10 SENSE - Not use, connect to TP7 52 EFM - Not use, connect to TP1 11 FLOCK - Not use, connect to TP5 54 VCOF2 I/O Loop filter pin for Digital 12 TLOCK - Not use, connect to TP5 55 SUBC Not use Not use 13 BLKCK O Sub code block clock is gnal output 57 VSS - GND for crystal oscillation circuit (1=6.394/MH2) 58 X1 1 Input for crystal oscillation circuit (1=6.394/MH2) 59 X2 O Output for crystal oscillation circuit (1=6.394/MH2) oscillation circuit 28 YA O Try tor crystal oscillation circuit (1=6.394/MH2) oscillation circuit 29 YA O <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>•</td> <td></td> | | | | | | | • | |
| elock signal input 48 PLLF I/O Loop filter pin for PLL 8 MDATA I Micro computer command 49 VCOF I/O Loop filter pin for VCO 9 MLD I Micro computer command 50 AVDD2 - Power supply for analog circuit 10 SENSE - Not use, connect to TP5 52 EFM - Not use, connect to TP1 11 FLOCK - Not use, connect to TP5 54 VCOF2 I/O Loop filter pin for Digital serve VCO 12 TLOCK - Not use, connect to TP4 55 SUBC - Not use 13 BLKCK O Sub code block clock - Sold use - 14 SOCK I External clock input for sub 56 SBCK - Not use 15 SUBQ O Sub code 10 ata output - circuit (=16.3944MHz) 16 DMUTE I Muting input (L: Read) 59 X2 O Output for crystal oscillation circuit (=16.3944MHz) 17 STAT | - | | | | | | | |
| 8 MDATA 1 Micro computer command data input 49 VCOF V/O Loop filter pin for VCO 9 MLD 1 Micro computer command load signal input (L: Load) 51 AVSS2 - RoW or supply for analog circuit 10 SENSE - Not use, connect to TP6 53 PCK O Clock output for PLL 11 FLOCK - Not use, connect to TP6 53 PCK O Clock output for PLL 12 TLOCK - Not use, connect to TP6 54 VCOF2 V/O Loop filter pin for Digital servo VCO 14 SQCK 0 Sub code Q data output 55 SUBC - Not use 15 SUB O Sub code Q data output circuit (1=16.9344MHz) circuit (1=16.9344MHz) 16 DMUTE 1 Muting input (L: Reset) 59 X2 O Output for crystal oscillation circuit (1=16.9344MHz) 20 PMCK - Not use 62 CLDCK O Sub code frame clock signal output | 7 | MCLK | I | | | | | |
| data input 50 AVDD2 - Power suppily for analog circuit load signal input (L Load) 9 MLD 1 Micro computer command load signal input (L Load) 51 AVD22 - RoN tor analog circuit 10 SENSE - Not use, connect to TP7 52 EFM - Not use, connect to TP5 11 FLOCK - Not use, connect to TP5 54 VCOF2 VO Look output for Digital servo VCO 12 TLOCK - Not use, conde to tP5 54 VCOF2 VO Not use 13 BLKCK O Sub code Q data output 56 SBCK - Not use 14 SOCK I External clock input for sub 56 SBCK - Not use 15 SUBC O Sub code Q data output 57 VSS - GND for crystal oscillation circuit (r=16.9344MHz) 18 RST I Reset signal input 60 VDD - Power suppil for crystal oscillation circuit (r=16.9344MHz) 19 </td <td></td> <td></td> <td></td> <td>clock signal input</td> <td>48</td> <td>PLLF</td> <td>I/O</td> <td>Loop filter pin for PLL</td> | | | | clock signal input | 48 | PLLF | I/O | Loop filter pin for PLL |
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| 40 LDON O Laser on signal output 80 SSEL I IOSEL=H, SMCK output, | 39 | 000 | ' | | 19 | INIGEL | ' | |
| | | | | | 00 | | , | |
| [[(H:ON) [SUBQ output mode select pin] | 40 | LDON | 0 | | 80 | SSEL | | |
| | | | | ((H:ON) | | | | SUBQ output mode select pin |

MX-K1R

TDA7295 (IC101, IC201) : Audio amplifier

1. Terminal layout



2. Block diagram

